Appl. No. 10/064,053
Amdt. dated June 30, 2005
Reply to Office action of March 30, 2005

**Amendments to the Specification:**

Please replace the title of the invention with the following amended title:

5    METHOD AND RELATED CIRCUIT FOR <u>GENERATING A WOBBLE CLOCK AND</u>
<u>AN ATIP CLOCK THROUGH A REFERENCE CLOCK AND A WOBBLE SIGNAL</u>
~~CLOCK GENERATION AND RECOVERY~~

Please add the following <u>new</u> paragraph after paragraph [0032]:

10   [0032.1] Fig.13 is a flowchart summarizing the present invention clock generation
        method.

Please add the following nine (9) <u>new</u> paragraphs after paragraph [0042]:

15   [0042.1] Please refer to Fig.13. Fig.13 is a flowchart summarizing the present invention
        clock generation method. Steps contained in the flowchart are described
        below.

     [0042.2] Step 100: Start.
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     [0042.3] Step 102: Use the counter 72 to count the number of reference periods of the
        reference clock 66 occurring within a period of the wobble signal 64 and
        generate a corresponding counting result 73.

25   [0042.4] Step 104: Calculate an average number 76 according to a long-term average of
        the counting results 73.

     [0042.5] Step 106: Generate a wobble clock 70 by dividing the reference clock 66 by the

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average number 76.

[0042.6] Step 108: Generate a comparing result with the comparator 78 by comparing the counting result 73 and the average number 76.

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[0042.7] Step 110: Generate the ATIP signal 80 by shaping a waveform of the comparing result generated by the comparator 78 through the wobble clock 70.

[0042.8] Step 112: Generate the ATIP clock 88 according to the ATIP signal 80 and the
10          wobble clock 70.

[0042.9] Step 114: End.

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